## JC20 Rec'd PCT/PTO 1 5 JUN 2005

PTO/SB/08A (08-03)

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Substitute for form 1449/PTO	Coi	mplete if Known
	Application Number	AU/220202
INFORMATION DISCLOSURE	Filing Date	Concurrently
	First Named Inventor	EDWIN RIJPKEMA
STATEMENT BY APPLICANT	Art Unit	N/A
(Use as many sheets as necessary)	Examiner Name	N/A
Sheet 1 of 1	Attorney Docket Number	NL021330

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Examiner Initials*	Cite No.1	Document Number  Number-Kind Code <sup>2 (# known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/ 1 1/	<del></del>	US 0004/000504 A4	40.05.0004	(4)4/4 DA4	
/J.K.		<sup>US-</sup> 2001/0033581 A1	10-25-2001	KAWARAI	
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	Cite No.1	Cite Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	Г	
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Substitute for form 1449/PTO		Complete il Known	•	
-	Application Number	107538563		
INFORMATION DISCLOSURE	Filing Date	Concurrently		
STATEMENT BY APPLICANT	First Named Inventor	EDWIN RIJPKEMA		
(Use as many sheets as necessary)	Art Unit	N/A		
	Examiner Name	N/A		
Sheet 1 of 1	Attorney Docket Number	NL021330		

		NON PATENT LITERATURE DOCUMENTS	
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/J.K./		A ROUTER ARCHITECTURE FOR NETWORKS ON SILICON by Edwin RIJPKEMA ET AL., Proceedings of Progress 2001, 2nd Workshop on Embedded Systems	
/J.K./		NETWORKS ON SILICON: COMBINING BEST-EFFORT AND GUARANTEED SERVICES, by K. GOOSSENS ET AL., Proceedings 2002 Design	
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	Application Number	10538563	
	Filing Date	2005-06-15	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	First Named Inventor Edwi	n Rijpkema	
(Not for submission under 37 CFR 1.99)	Art Unit	2161	
(1100)	Examiner Name		
	Attorney Docket Number	NL021330	

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( Not for submission under 37 CFR 1.99)

Application Number		10538563	
Filing Date		2005-06-15	
First Named Inventor	Edwi	n Rijpkema	
Art Unit		2161	
Examiner Name			
Attorney Docket Numb	er	NL021330	

/J.K./	1	RIJPKEMA E ET AL; A Router Architecture for Networks on Silicon: Proceedings of Progress 2001, 2nd Workshop on Embedded Systems; November 2001; pages 181 - 188; Veldhoven, NL					
/J.K./	GOSSENS K ET AL: Networks on Silicon; Combining Best-Effort and Quaranteed Services: Proceedings 2002 Design, March 4 8 2002; pages 423-425 Los Alamitos, CA USA, IEEE Comput.						
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